



# Shri Phaniswvar Nath Renu Engineering College, Araria

(Under Department of Science & Technology Patna, Govt. of Bihar)

Ref. No: **SPNREC/347**  
Room No: 04

Time Table, SPNREC ARARIA

Revised 3<sup>rd</sup> SEM, Electronics & Communication ENGINEERING

W.E. F: 10-05-2023

Date: 03/05/2023

Day	Period 1	Period 2	Period 3	Period 4	Period 5	Period 6
Mon	Object Oriented Programming	Signal & System	Network Theory	Maths-III	Basic Electronics LAB	
Tue	Maths-III	Signal & System	Object Oriented Programming	Language lab	Electrical & Electronic Material	
Wed	Maths-III	Signal & System	Object Oriented Programming	Electrical & Electronic Material	Electrical & Electronic Material Lab	
Thu	Object Oriented Programming Lab			Network Theory	Electrical & Electronic Material Lab	
Fri	Maths-III	Basic Electronics	Network Theory -	Internship	Language lab	
Sat	Basic Electronics			Network Theory	Internship	

Course Title	Faculty Name
Signal & System	Dr. Ritesh Kumar, Assistant Prof., EC
Object Oriented Programming	Mr. Sriam Yadav, GAP
Basic Electronics	Prof. Amit Ranjan, Assistant Prof., EC
Network Theory	Prof. Amit Ranjan, Assistant Prof., EC
Electrical & Electronic Material	Dr. Ritesh Kumar, Assistant Prof., EC
Maths-III	Mr. Nikunj Kumar, GAP
Language lab	Mr. Saurabh Gupta, GAP
Internship	Prof. <del>Kumar</del> Assistant Prof., EC

Professor I/C  
Time Table

*Y.N.V.*

Principal  
SPNREC ARARIA

E-mail: [spnreca@gmail.com](mailto:spnreca@gmail.com)



# Shriphanishwar NathRenu Engineering College, Araria

(Under Department of Science & Technology Patna, Govt. of Bihar)

Ref. No: **SPNREC/348**  
Room No: 04

**Time Table, SPNREC ARARIA**  
Revised 5<sup>th</sup> SEM, Electronics & Communication ENGINEERING

Date: **09/05/2023**  
W.E. F: 10-05-2023

Day	Period 1	Period 2	Period 3	1:00-2:00	Period 4	Period 5	Period 6
Mon	Linear Integrated Circuit	Digital Signal Processing	Microprocessor & Microcontroller	LUNCH BREAK	Linear Control System	Microprocessor & Microcontroller Lab	
Tue	Computer Network & Security	Digital Signal Processing	Linear Control System		Linear Integrated Circuit Lab	Computer Network & Security	
Wed	Linear Integrated Circuit	Digital Signal Processing					
Thu	Linear Integrated Circuit	Linear Control System	Probability & Stochastic Processes		Microprocessor & Microcontroller		
Fri	Probability & Stochastic Processes	Microprocessor & Microcontroller	Linear Control System		Digital Signal Processing Lab		
Sat	Probability & Stochastic Processes		Computer Network & Security		Internship & Software Skills		

Course Title	Faculty Name
Digital Signal Processing	Prof. Dharmendra Kumar, Asst Prof. ECE
Linear Integrated Circuit	Prof. Kumar Vimal, Asst Prof. ECE
Microprocessor & Microcontroller	Prof. Shahin, Asst Prof. ECE
Linear Control System	Prof. Priyatam Kumar, Assistant Prof., ECE
Probability & Stochastic Processes	Prof. Raj Kumar Sharma, Assistant Prof., ECE
Computer Network & Security	Mr. Sriram Yadav, GAP
Internship & Software Skills	Dr. Ritesh Kumar, Assistant Prof., ECE

*K Vimal*

*AR*

Professor I/C  
Time Table

Principal  
SPNREC ARARIA



# ShriPhaniswarNathRenu Engineering College, Araria

(Under Department of Science & Technology Patna, Govt. of Bihar)

Ref. No: SPNREC/1345  
Room No: 06

Time Table, SPNREC ARARIA

Revised 3<sup>rd</sup> SEM, Mechanical ENGINEERING

W.E. F.: 10-05-2023

Date: 09/05/2023

	Period 1	Period 2	Period 3		Period 4	Period 5	Period 6
Day	10:00-11:00	11.00-12.00	12:00-1:00	1:00-2:00	2:00-3:00	3:00-4:00	4:00-5:00
Mon	Thermodynamics	Engineering Mechanics	Basic Electronics	LUNCH BREAK	Maths-III	Spoken Tutorial/ KYP	Biology
Tue	Thermodynamics	Basic Electronics	Engineering Mechanics		Maths-III	Machine Drawing Lab	
Wed	Thermodynamics	Basic Electronics	Maths-III		Biology		
Thu	Thermodynamics	Biology	-		Engineering Mechanics Lab		
Fri	Spoken Tutorial/KYP	Maths-III	-		-	Machine Drawing Lab	
Sat	Engineering Mechanics	Machine Drawing Lab	-		-	Engineering Mechanics Lab	

Course Title	Faculty Name
Thermodynamics	Prof. Abhay Kumar Singh, Asst Prof. ME. & Prof. Bihar Gaurav, Asst Prof. ME
Engineering Mechanics	Prof. Manish Kumar Jaiswal, Asst Prof, ME
Basic Electronics	Prof. Kumar Vimal, Asst Prof. ECE
Maths-III	Mr. Nikunj Kumar, GAP
Spoken Tutorial/KYP	Mr. Saurabh Gupta, GAP
Biology	-
Machine Drawing	Prof. Manish Kumar Jaiswal, Asst Prof, ME

KV 10/05/2023

Professor I/C  
Time Table

AK  
09/05/2023  
Principal  
SPNREC ARARIA





# Shri Phanishwar Nath Renu Engineering College, Araria

(Under Department of Science & Technology Patna, Govt. of Bihar)

Ref. No: **SPNREC/350**  
Room No: 08

**Time Table, SPNREC ARARIA**  
Revised 5<sup>th</sup> SEM, Mechanical ENGINEERING

Date: **09/05/2023**  
W.E. F: 10-05-2023

Day	Period 1 10:00-11:00	Period 2 11.00-12.00	Period 3 12:00-1:00	1:00-2:00	Period 4 2:00-3:00	Period 5 3:00-4:00	Period 6 4.00-5:00
Mon	HMT	FMC	KOM	LUNCH BREAK	-	MP Lab	
Tue	MP	-	KOM		-	FMC Lab	
Wed	-	HMT	FMC		-	HMT Lab	
Thu	KOM	MP	FM		SOM	MP Lab	
Fri	MP	HMT	-		-	HMT Lab	
Sat	-	FMC	KOM		-	FMC Lab	

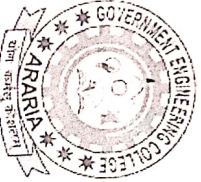
Course Title	Short Title	Faculty Name
Heat & Mass Transfer	HMT	Prof. Farhan Ali Ahmad, Asst Prof ME/Prof. Abhay Kumar Singh, Asst Prof. ME.
Fluid Machinery	FM	Prof. Md Shahbeen Raza, Asst Prof. ME
Manufacturing Process	MP	Prof. Aamir Sohail, Asst Prof. ME
Kinematics of Machine	KOM	Prof. Manish Kumar Jaiswal, Asst Prof, ME
MOOC/SWYAM Courses	OE-1	Prof. Abhay Kumar Singh, Asst Prof. ME.

*Y.N.R.S*

*AR*

Professor I/C  
Time Table

Principal  
SPNREC ARARIA



# Shri Phanishwar Nath Renu Engineering College, Araria

(Under Department of Science & Technology Patna, Govt. of Bihar)

Ref. No. SPNREC/353

Revised Time Table, SPNREC ARARIA

Date: 10/05/2023  
W.E. F: 10/05/2023

Room no:03

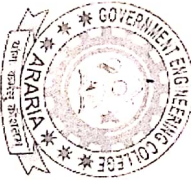
3<sup>rd</sup> SEM, ELECTRICAL ENGINEERING

Day	Period 1 10:00-11:00	Period 2 11:00-12:00	Period 3 12:00-1:00	BREAK 1:00-2:00	Period 4 2:00-3:00	Period 5 3:00-4:00	Period 6 4:00-5:00
Mon	Analog Electronics	Electrical circuit Analysis	Electromagnetic Field	Lunch Break	Electrical Machine-I	Engineering Mechanics	Environmental Science
Tue	Analog Electronics	Engineering Mechanics	Electromagnetic Field		Electrical Machine-I	Electrical Machine-I LAB	Electrical Circuit Analysis LAB
Wed	Analog Electronics	Electromagnetic Field	Electromagnetic Field	Lunch Break	Electrical Machine-I	Electrical Circuit Analysis LAB	
Thu	Electrical circuit Analysis	Engineering Mechanics	Internship		Environmental Science	Analog Electronics LAB	Internship
Fri	Electrical circuit Analysis	Electrical Machine-I	Software Training LAB	Lunch Break	Environmental Science	Software Training LAB	
Sat	Electrical circuit Analysis	Engineering Mechanics	Software Training LAB		Environmental Science	Software Training LAB	

Course Title	Faculty Name
Analog Electronics (L+P)	Prof. Prayatan Kumar, Asst. Prof. ECE
Electrical circuit Analysis (L+P)	Mr. Pranav Prakash Singh, Guest Asst. Prof. EE
Engineering Mechanics	Prof. Farhan Ali Ahmad, Asst. Prof. ME
Electrical Machine-I	Prof. Arjun Kumar, Asst. Prof. EE
Electromagnetic Field	Mr. Kumar Shanu, Guest Asst. Prof. EE
Internship	Prof. Arjun Kumar, Asst. Prof. EE
Software Training LAB	Mr. Prem Anand, Guest Asst. Prof. CSE
Environmental Science	Mr. Shri Rann Yadav, Guest Asst. Prof. CSE

Professor I/C  
Time Table

Principal  
SPNREC Araria



# Shri Phani Shwar Nath Renu Engineering College, Araria

(Under Department of Science & Technology Patna, Govt. of Bihar)

Ref. No. SPNREC/354

Date: 10/05/2023  
W.E. F: 10/05/2023

Room no:07

Revised Time Table, SPNREC ARARIA  
5<sup>th</sup> SEM, ELECTRICAL ENGINEERING

Day	Period 1 10:00-11:00	Period 2 11:00-12:00	Period 3 12:00-1:00	BREAK 1:00-2:00	Period 4 2:00-3:00	Period 5 3:00-4:00	Period 6 4:00-5:00
Mon	Power Electronics	Power System-I	Microprocessor	Lunch break	Wind and Solar Energy System	Power System-I LAB	
Tue	Control System	Power System-I	Microprocessor		Power Electronics	Microprocessor LAB	
Wed	Control System	Power System-I	Microprocessor	Lunch break	Control System LAB		
Thu	Power Electronics	Control System	Wind and Solar Energy System		Power Electronics LAB		
Fri	Power Electronics	Control System	Wind and Solar Energy System	Lunch break	Internship	Power System-I LAB	
Sat	Power Electronics LAB					Microprocessor LAB	

Course Title	Faculty Name
Power System-I	Dr. Atma Ram Gupta, Principal
Wind and Solar Enrgy System	Mr. Pranav Prakash Singh, Guest Asst. Prof. EE
Control System	Mr. Pranav Prakash Singh, Guest Asst. Prof. EE
Microprocessor	Prof. Dharendra Kumar, Asst. Prof. ECE
Power Electronics	Mr. Kumar Shannu, Guest Asst. Prof. EE
Internship	Prof. Arjun Kumar, Asst. Prof. EE

10/05/2023  
Professor I/C  
Time Table

10/05/2023  
Principal  
SPNREC ARARIA